

11046 U.S. PTO
10/082311
02/26/02

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL. NUM 10082311	FILING DATE 02/26/2002	CLASS 438	SUBCLASS 597	GAU 2812	EXAMINER Burley
**APPLICANTS: Saikawa Takeshi; Maeno Ryohei; Okudaira Sadayuki; Saito Tetsuo; Tamaru Tsuyoshi; Ohmori Kazutoshi;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-140622 05/11/2001					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO XA-9635	
Verified and Acknowledged Examiners's initials					
TITLE : Manufacturing method of semiconductor device					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheet Dwg.	Figs. Dwg. Print Fig.
		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM
(Attached in pocket on right inside flap)